

Figure 1
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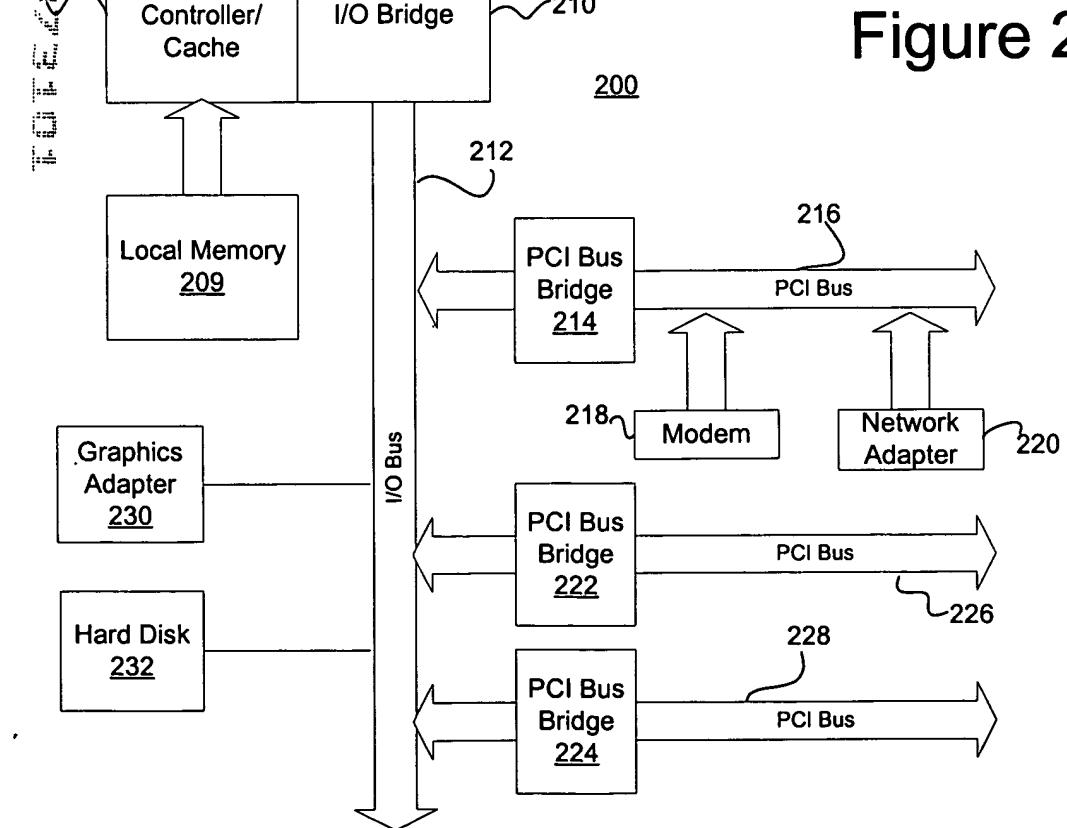
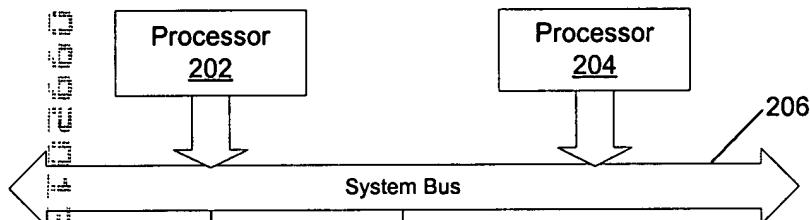
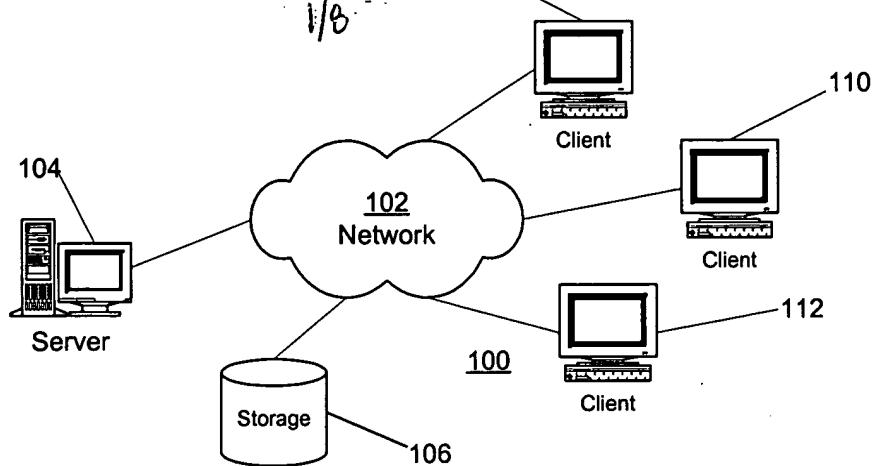
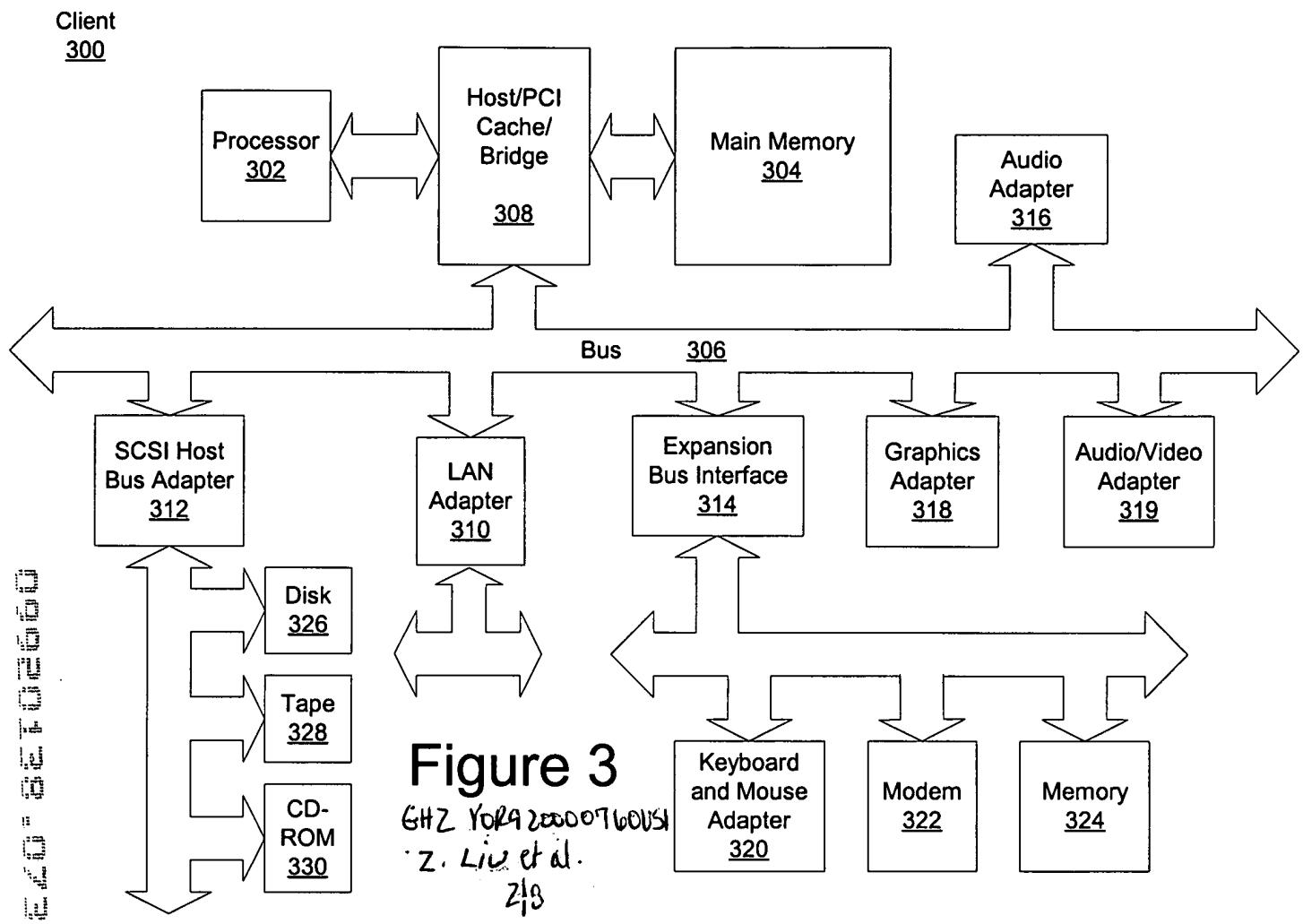


Figure 2



Generated logARMA(1,1) Delay Process and Its Convex Hull

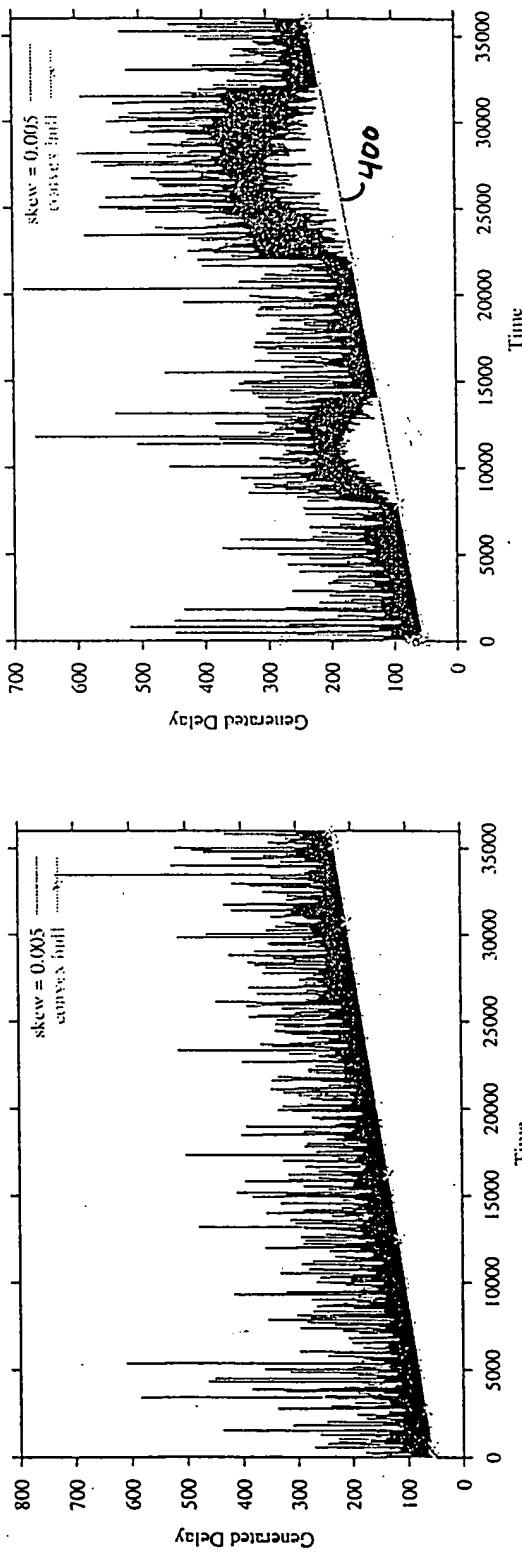


Figure 4A

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Figure 4B

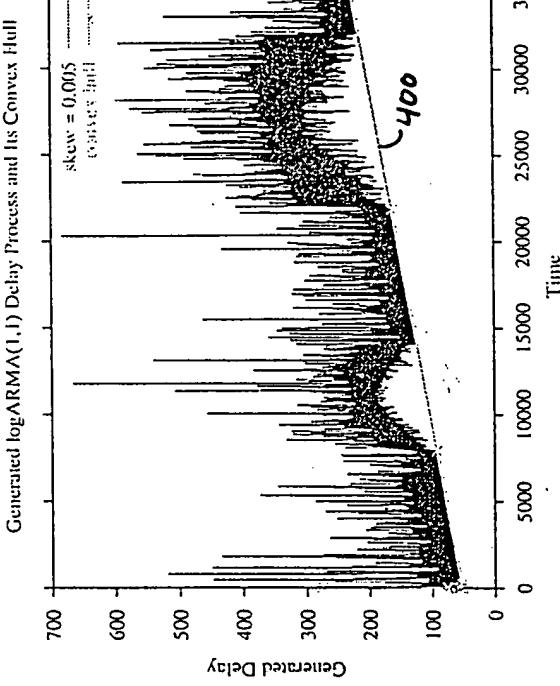


FIGURE 5

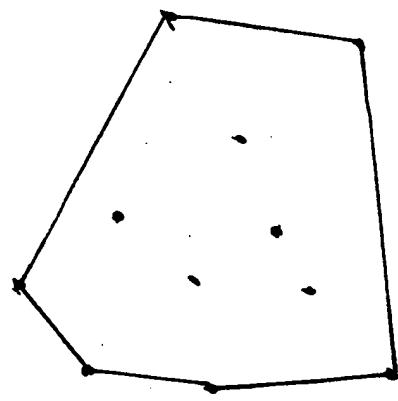


FIGURE 5
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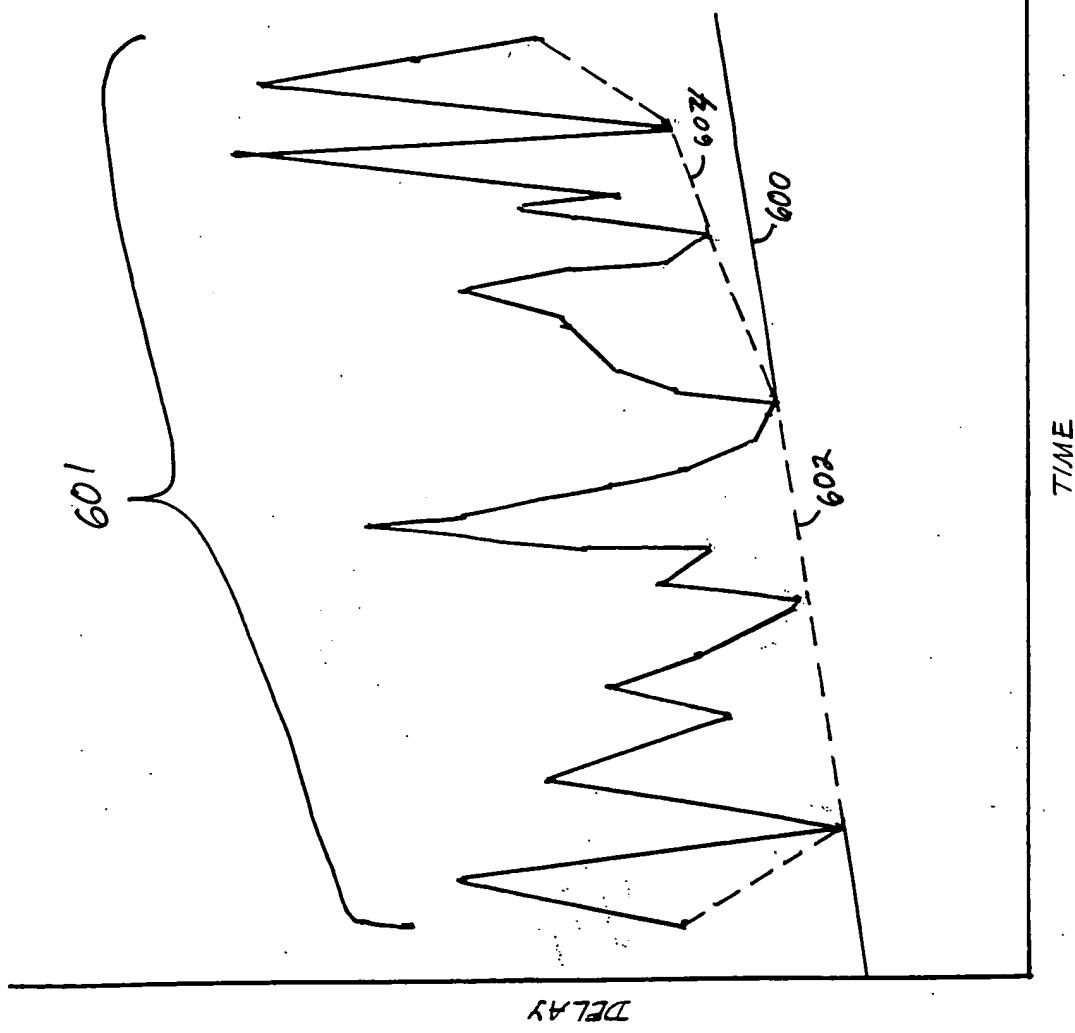


FIGURE 6
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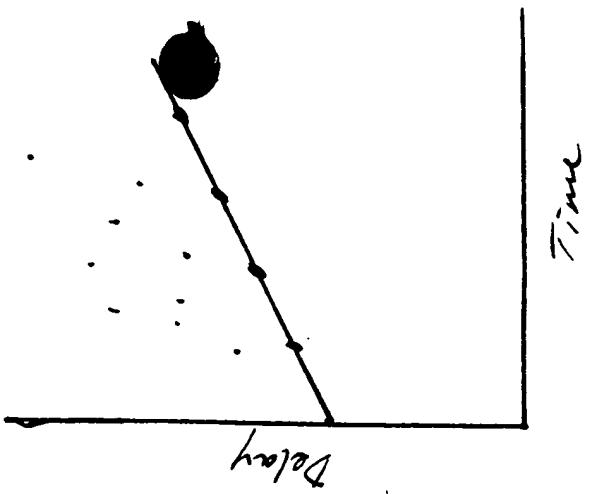


Figure 7

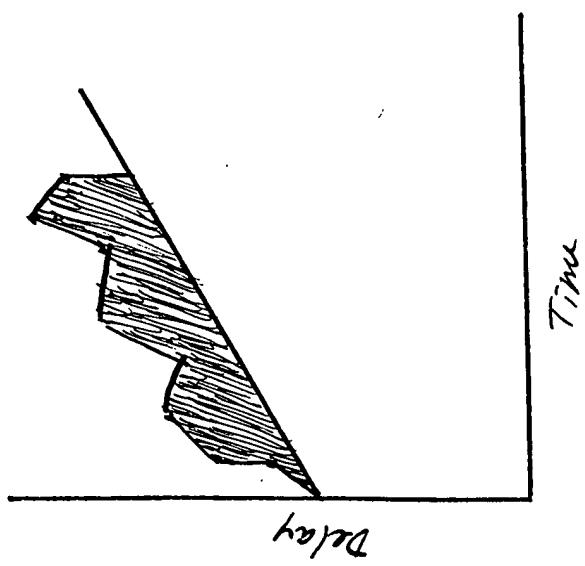


Figure 7B

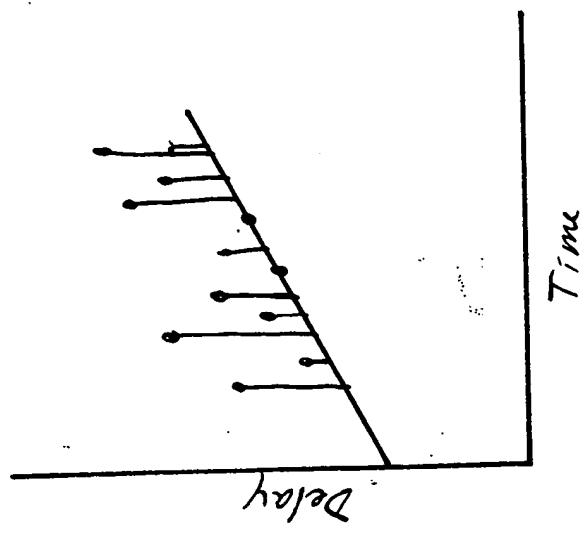


Figure 7A
GHz 10920000760001
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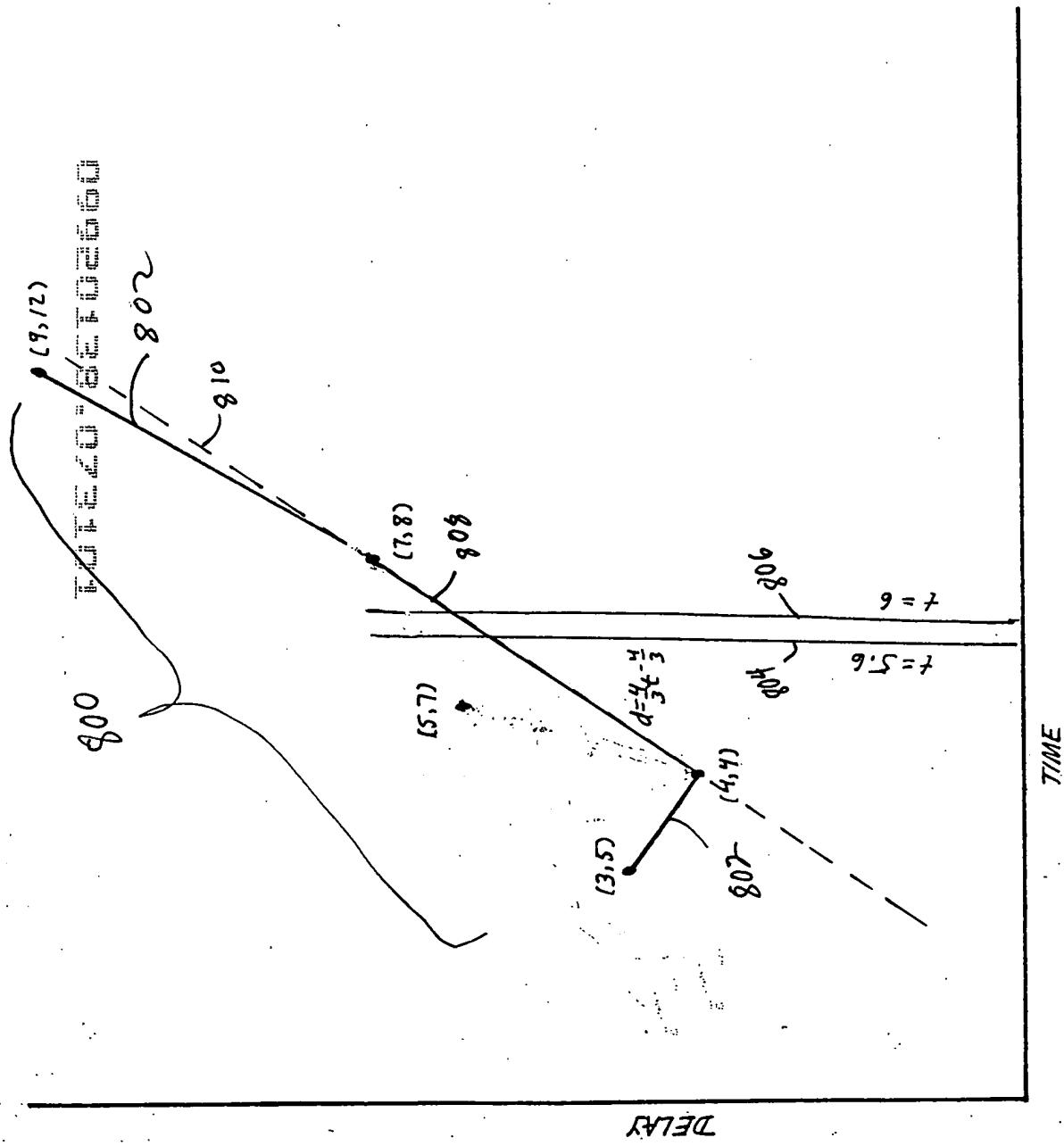


Figure 8
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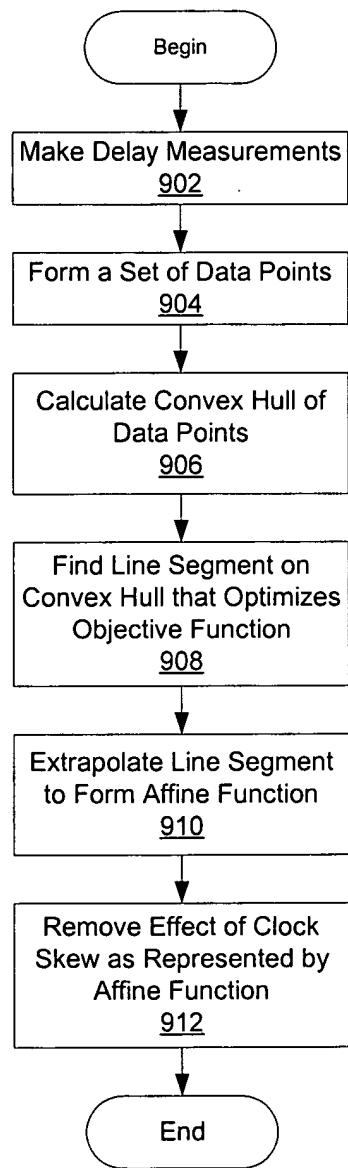


Figure 9
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